

High-Voltage Sense and Low-IQ Supervisory Circuits with Programmable Reset Delay

Features

- Qualified for Automotive Applications
 - AEC-Q100 Grade 1: $T_A = -40^{\circ}\text{C}$ to 125°C
- Supply Voltage Range: 2.4 V to 5.5 V (SENSE Pin Version)
- Wide Supply Voltage Range: 2.4 V to 36 V (VDD Pin Version)
- High Sense Voltage up to 36 V for the Sense Pin
- Very Low Quiescent Current: 2 μA Typical
- Fixed Threshold Voltage from 3.0 V to 12.0 V with 100-mV Step
- High Threshold Accuracy $\pm 1.5\%$ Typical
- Open-Drain Active Low $\overline{\text{RESET}}$ Output
- Green Product, SOT23-6 Package

Applications

- Automotive ECU
- EV Inverter
- Battery Charger Unit

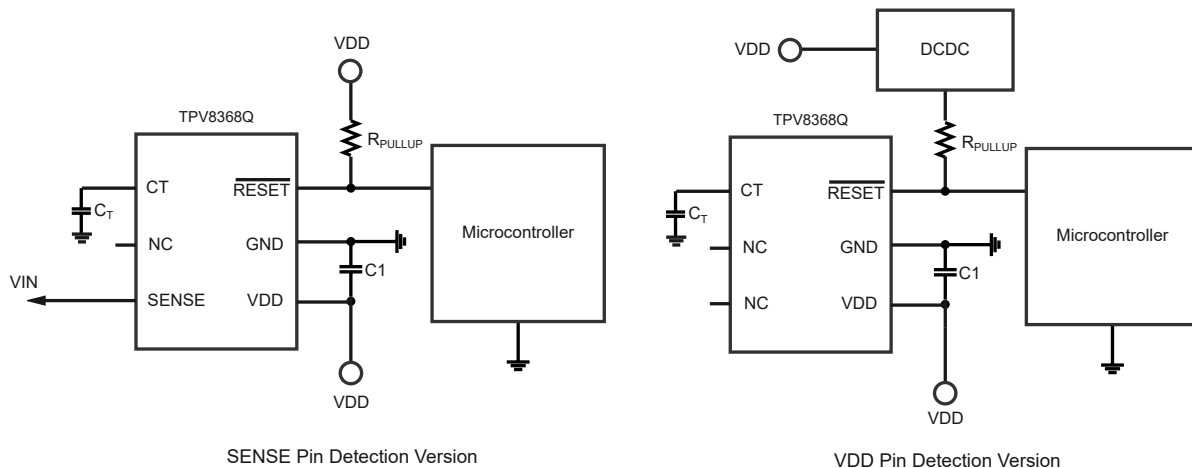
Description

The TPV8368Q is a family of the supervisory circuits to monitor a voltage rail from 3.0 V to 12.0 V, asserting an active low open-drain $\overline{\text{RESET}}$ output when the voltage of the sense voltage (SENSE pin version) drops below a fixed threshold or when the VDD voltage (VDD pin version) drops below a fixed threshold. The $\overline{\text{RESET}}$ output remains low for the user-adjusted delay time by the external capacitor after the monitored voltage returns above the threshold with hysteresis.

The threshold voltage of the TPV8368Q device can achieve $\pm 1.5\%$ accuracy. The TPV8368Q has a very low typical quiescent current of 2 μA .

The TPV8368Q is available in the SOT23-6 package. Its operating temperature range is from -40°C to $+125^{\circ}\text{C}$.

Typical Application Circuit



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Programmable Reset Delay****Table of Contents**

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High-Voltage Sense and Low-IQ Supervisory Circuits with Programmable Reset Delay

Product Family Table

TPV8368 xx Q - S6TR-S

Package
S6TR : SOT23-6

Version Code
SA : 3.0V detection voltage with sense pin
S : 100mV Step
YZ : 12V detection voltage

Device	Threshold Voltage (V _{IT})	Sense Pin	Marking	Package
TPV8368SAQ-S6TR-S	3.00	Sense	Q00	SOT23-6
TPV8368SKQ-S6TR-S ⁽¹⁾	4.00	Sense	Q10	SOT23-6
TPV8368SPQ-S6TR-S ⁽¹⁾	4.50	Sense	Q15	SOT23-6
TPV8368SZQ-S6TR-S ⁽¹⁾	5.50	Sense	Q25	SOT23-6
TPV8368TBQ-S6TR-S ⁽¹⁾	5.70	Sense	Q27	SOT23-6
TPV8368TYQ-S6TR-S ⁽¹⁾	8.00	Sense	Q50	SOT23-6
TPV8368USQ-S6TR-S ⁽¹⁾	10.00	Sense	Q70	SOT23-6
TPV8368VMQ-S6TR-S ⁽¹⁾	12.00	Sense	Q90	SOT23-6
TPV8368VNQ-S6TR-S ⁽¹⁾	3.00	VDD	Q00	SOT23-6
TPV8368VXQ-S6TR-S ⁽¹⁾	4.00	VDD	Q10	SOT23-6
TPV8368WCQ-S6TR-S ⁽¹⁾	4.50	VDD	Q15	SOT23-6
TPV8368WMQ-S6TR-S ⁽¹⁾	5.50	VDD	Q25	SOT23-6
TPV8368WOQ-S6TR-S ⁽¹⁾	5.70	VDD	Q27	SOT23-6
TPV8368XLQ-S6TR-S ⁽¹⁾	8.00	VDD	Q50	SOT23-6
TPV8368YFQ-S6TR-S ⁽¹⁾	10.00	VDD	Q70	SOT23-6
TPV8368YZQ-S6TR-S ⁽¹⁾	12.00	VDD	Q90	SOT23-6

(1) For new threshold voltage configurations, please contact the 3PEAK.

Revision History

Date	Revision	Notes
2020-08-05	Rev.Pre.0	Pre-Release Version.
2021-08-20	Rev.Pre.1	Updated Part Name.
2021-09-14	Rev.Pre.2	Changed Propagation delay.
2021-10-19	Rev.Pre.3	Changed EC according to test result.
2022-01-14	Rev.Pre.4	Updated tape and reel information.
2022-06-20	Rev.Pre.5	Updated thermal resistance and application circuit.
2022-12-01	Rev.A.0	Added reset timing chart and reset delay time equation.
2023-07-07	Rev.A.1	Update typical application circuit.

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Pin Configuration and Functions

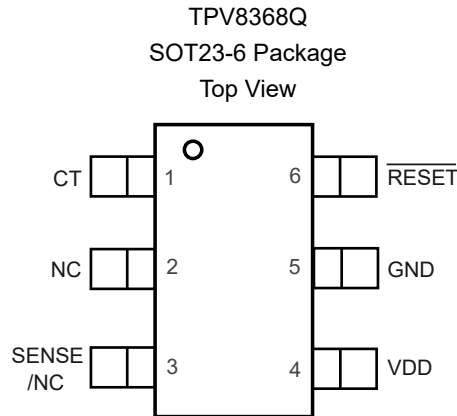


Table 1. Pin Functions: TPV8368Q

Pin		I/O	Description
NO.	Name		
4	VDD	P	Supply Voltage. A 0.1- μ F ceramic capacitor is placed as close as to the V _{DD} pin.
3	SENSE/ NC	I	SENSE Pin. It is used for monitoring voltage. If the voltage drops below the threshold voltage V _{IT} , the $\overline{\text{RESET}}$ is asserted. This pin is NC in the V _{DD} detection version device.
1	CT	I/O	Reset Delay Time Programming Pin. Connecting this pin to ground referenced capacitor (≥ 100 pF) gives a user-programmable reset delay time.
2	NC	-	Not connected.
6	$\overline{\text{RESET}}$	O	$\overline{\text{RESET}}$ Output. This pin is an active low open drain output or push-pull output. It is driven to a low impedance state when $\overline{\text{RESET}}$ is asserted by the voltage of the SENSE pin lower than the threshold V _{IT} or the VDD voltage lower than the threshold. $\overline{\text{RESET}}$ is low for the reset delay time programmed by the CT pin after the monitored voltage is above V _{IT} . A pulled-up resistor from 10 k Ω to 1 M Ω should be connected to VDD if it is open drain output.
5	GND	G	Ground. This pin should be connected to ground reference.

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Specifications

Absolute Maximum Ratings

Parameter		Min	Max	Unit
	VDD to GND, SENSE Pin Detection	-0.3	6.5	V
	VDD to GND, VDD Pin Detection	-0.3	45	V
	Input Voltage of the SENSE Pin	-0.3	45	V
	Voltage of CT Pin	-0.3	6.5	V
	Output Voltage of $\overline{\text{RESET}}$ Pin	-0.3	6.5	V
	Current of $\overline{\text{RESET}}$ Pin		20	mA
T _J	Maximum Junction Temperature	-40	125	°C
T _A	Operating Temperature Range	-40	125	°C
T _{STG}	Storage Temperature Range	-65	150	°C
T _L	Lead Temperature (Soldering 10 sec)		260	°C

(1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

(2) This data was taken with the JEDEC low effective thermal conductivity test board.

(3) This data was taken with the JEDEC standard multilayer test boards.

ESD, Electrostatic Discharge Protection

Parameter		Condition	Minimum Level	Unit
HBM	Human Body Model ESD	ANSI/ESDA/JEDEC JS-001 ⁽¹⁾	±2000	V
CDM	Charged Device Model ESD	ANSI/ESDA/JEDEC JS-002 ⁽²⁾	±1000	V

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

(2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

Recommended Operating Conditions

Parameter		Min	Typ	Max	Unit
V _{DD}	VDD Pin Detection	2.4		36	V
V _{DD}	SENSE Pin Detection	2.4		5.5	V
V _{SENSE}		0		36	V

Thermal Information

Package Type	θ _{JA}	θ _{JC}	Unit
SOT23-6	143.9	67.4	°C/W

High-Voltage Sense and Low-IQ Supervisory Circuits with Programmable Reset Delay

Electrical Characteristics

All test conditions: $V_{DD} = 2.4 \text{ V to } 36 \text{ V}$ (VDD Pin Detection), $T_a = -40^\circ\text{C to } 125^\circ\text{C}$, $R_{PULLUP} = 10 \text{ k}\Omega \text{ to } V_{DD}$, $C_{CT} = 1000 \text{ pF}$, typical values are at $T_a = +25^\circ\text{C}$, unless otherwise noted.

Parameter		Conditions	Min	Typ	Max	Unit
DC Specifications						
V_{DD}	Supply Voltage Range (VDD Pin Detection)	$-40^\circ\text{C} < T_a < 125^\circ\text{C}$	2.4		36.0	V
I_{DD}	Quiescent Current (I_Q)	$V_{DD} = V_{IT} - 0.1 \text{ V}$		2.0	7.0	μA
		$V_{DD} = V_{IT} + 1.0 \text{ V}$		2.0	7.5	μA
I_{OL}	Output Current of $\overline{\text{RESET}}$ pin	$V_{DD} = 4.5 \text{ V}, V_{DS} = 0.05 \text{ V}$			2	mA
$V_{IT, ERR}$	Negative-going Input Threshold Accuracy	$T_a = 25^\circ\text{C}$	-1.5		1.5	%
		$-40^\circ\text{C} < T_a < 125^\circ\text{C}$	-3.0	± 1.5	3.0	%
V_{HYS}	Hysteresis on V_{IT}	VDD pin detection		5	5.5	%
		VDD pin without hysteresis	0		10	mV
R_{CTDIS}	CT Pin Discharge NMOS on Resistance	$V_{DD} = 13.0 \text{ V}, V_{CD} = 0.5 \text{ V}$	0.5		3.4	k Ω
Switching Electrical Specifications						
t_D	Reset Delay Time	$C_T = 1000 \text{ pF}$	Guaranteed by design and characterization		10	ms
		$C_T = 10 \text{ nF}$			100	ms
t_{PHL}	Propagation Delay	$V_{IH} = 1.05 V_{DD}, V_{IL} = 0.95 V_{DD}$		20		μs

High-Voltage Sense and Low-IQ Supervisory Circuits with Programmable Reset Delay

All test conditions: $V_{DD} = 2.4\text{ V to }5.5\text{ V}$ (SENSE Pin Detection), $T_a = -40^\circ\text{C to }125^\circ\text{C}$, $R_{PULLUP} = 10\text{ k}\Omega$ to V_{DD} , $C_{CT} = 1000\text{ pF}$, typical values are at $T_a = +25^\circ\text{C}$, unless otherwise noted.

Parameter		Conditions	Min	Typ	Max	Unit
DC Specifications						
V_{DD}	Supply Voltage Range (VDD Pin Detection)	$-40^\circ\text{C} < T_a < 125^\circ\text{C}$	2.4		5.5	V
I_{DD}	Quiescent Current (I_Q)	$V_{SENSE} = V_{IT} - 0.1\text{ V}$		2.0	5.5	μA
		$V_{SENSE} = V_{IT} + 1.0\text{ V}$		2.0	6.2	μA
R_{SENSE}	SENSE Resistance		5		68	$\text{M}\Omega$
I_{OL}	Output Current of $\overline{\text{RESET}}$ Pin	$V_{DD} = 4.5\text{ V}$, $V_{DS} = 0.05\text{ V}$, $V_{SENSE} = V_{IT} - 0.1\text{ V}$			2	mA
$V_{IT,ERR}$	Negative-going Input Threshold Accuracy	$T_a = 25^\circ\text{C}$	-1.5		1.5	%
		$-40^\circ\text{C} < T_a < 125^\circ\text{C}$	-3.0	± 1.5	3.0	%
V_{HYS}	Hysteresis on V_{IT} (SENSE Pin)			5	5.5	%
R_{CTDIS}	CT Pin Discharge NMOS on Resistance	$V_{DD} = 4.5\text{ V}$, $V_{SENSE} = 13.0\text{ V}$, $V_{CD} = 0.5\text{ V}$	0.5		3.4	$\text{k}\Omega$
Switching Electrical Specifications						
t_D	Reset Delay Time	$C_T = 1000\text{ pF}$	Guaranteed by design and characterization		10	ms
		$C_T = 10\text{ nF}$			100	ms
t_{PHL}	Propagation Delay	$V_{IH} = 1.05 V_{DD}$, $V_{IL} = 0.95 V_{DD}$		20		μs

High-Voltage Sense and Low-IQ Supervisory Circuits with Programmable Reset Delay

Typical Performance Characteristics

All test conditions: $V_{DD} = 5\text{ V}$, $V_A = +25^\circ\text{C}$, unless otherwise noted.

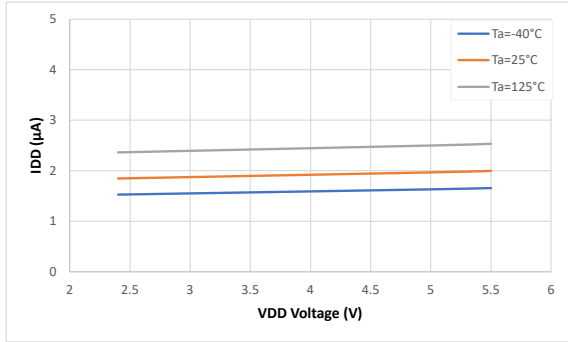


Figure 1. Supply Current vs. Input Voltage ($V_{SENSE} = V_{IT} + 1\text{ V}$)

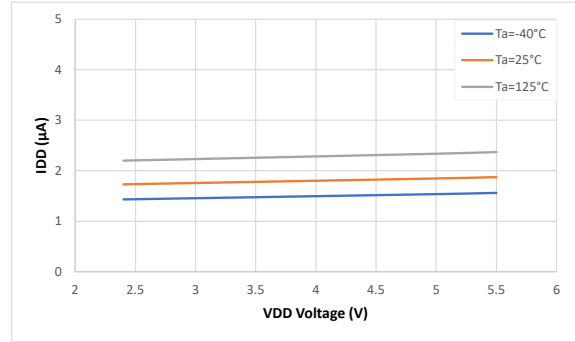


Figure 2. Supply Current vs. Input Voltage ($V_{SENSE} = V_{IT} - 0.1\text{ V}$)

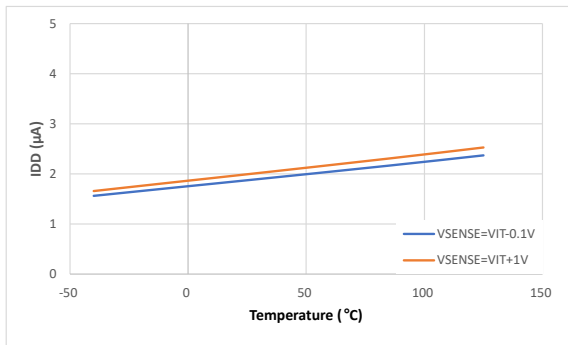


Figure 3. Supply Current vs. Temperature

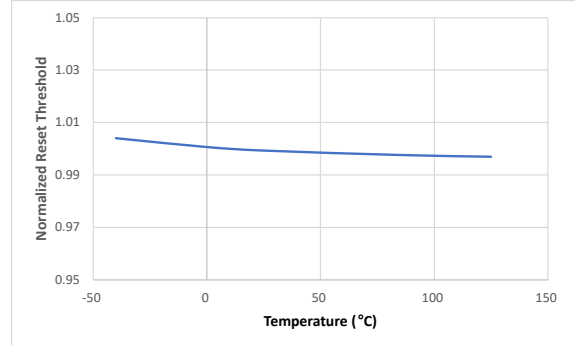


Figure 4. Normalized Reset Threshold vs. Temperature

High-Voltage Sense and Low-IQ Supervisory Circuits with Programmable Reset Delay

Detailed Description

Overview

The TPV8368Q is a family of the supervisory circuits to monitor a voltage rail from 3.0 V to 12.0 V, asserting an active low $\overline{\text{RESET}}$ output when the voltage of the SENSE pin drops below a fixed threshold (SENSE pin version) or when the VDD voltage drops below a fixed threshold (VDD pin version). The $\overline{\text{RESET}}$ output remains low for the user adjusted delay time by the external capacitor after the monitored voltage returns above the fixed threshold with hysteresis.

Functional Block Diagram

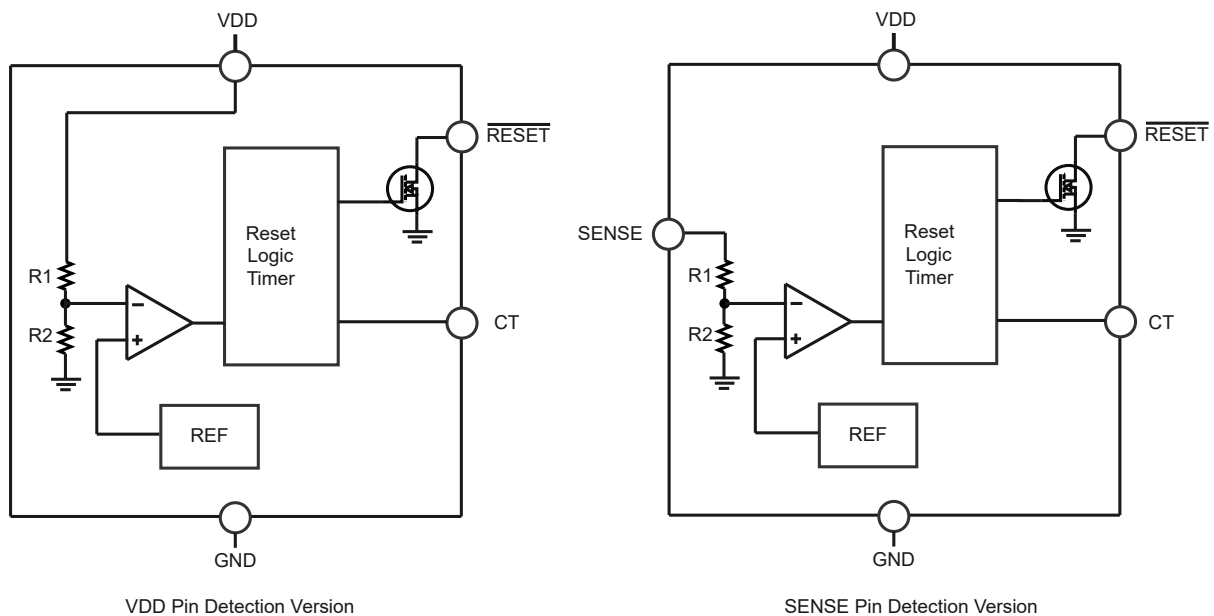


Figure 5. Functional Block Diagram

High-Voltage Sense and Low-IQ Supervisory Circuits with Programmable Reset Delay

Feature Description

RESET Output

TPV8368Q features an active-low output, the reset signal is low for the SENSE pin voltage (SENSE pin version) drops below V_{IT} or the VDD pin voltage (VDD pin version) drops below V_{IT} . The reset remains asserted for the duration of the reset delay time (t_D) after the monitored voltage rises above the reset threshold. The two figures below show the reset outputs.

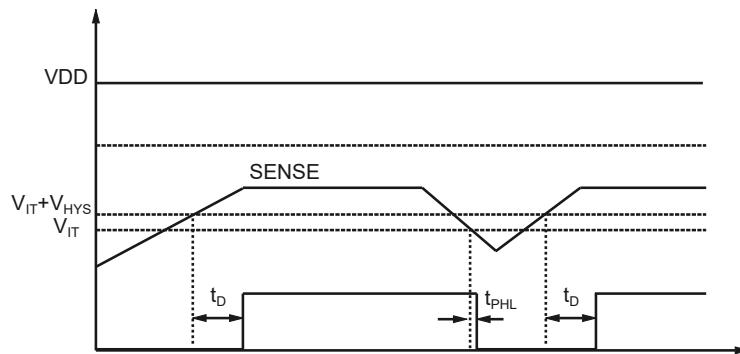


Figure 6. SENSE Pin Version Reset Timing

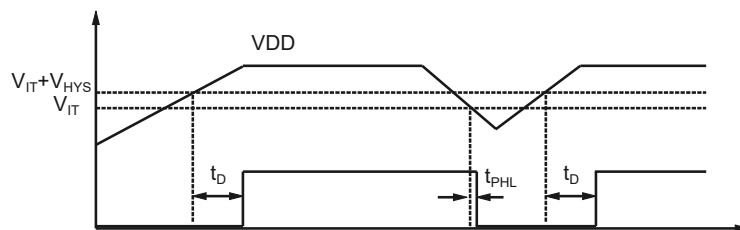


Figure 7. VDD Pin Version Reset Timing

RESET Delay Time

The TPV8368Q provides programmable reset delay time (t_D), which is realized by selecting a capacitor between CT and GND, the reset delay time (t_D) under given capacitor value is calculated using the [Equation 1](#).

$$t_D \text{ (ms)} = 10 \times C_{CT} \text{ (nF)} \tag{1}$$

High-Voltage Sense and Low-IQ Supervisory Circuits with Programmable Reset Delay

Application and Implementation

Note

Information in the following application sections is not part of the 3PEAK's component specification and 3PEAK does not warrant its accuracy or completeness. 3PEAK's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

Application Information

The TPV8368Q is a family of supervisory circuits to monitor a wide voltage rail from 3.0 V to 12.0 V, and assert an active low open drain output when the reset threshold is triggered. The reset delay time can be programmed by an external capacitor.

Typical Application

The following figures show the typical application schematic.

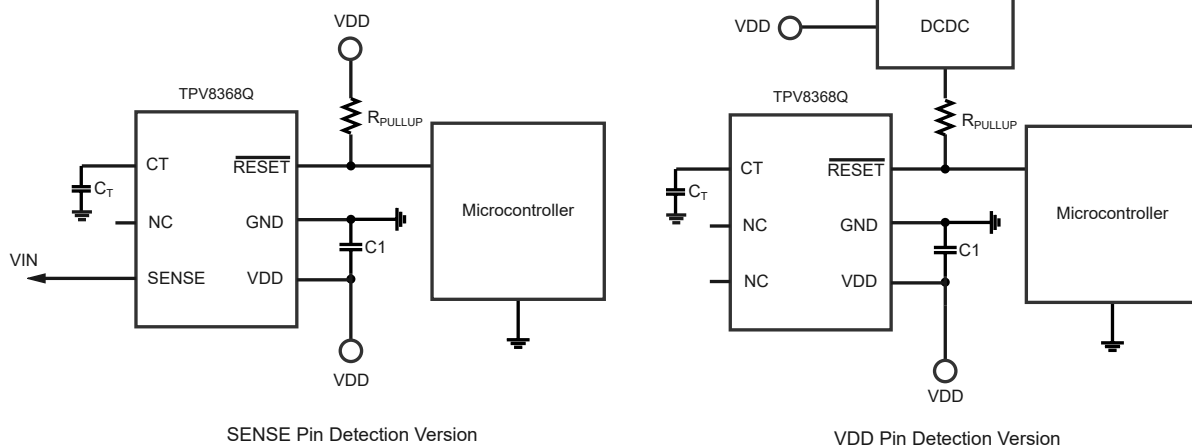
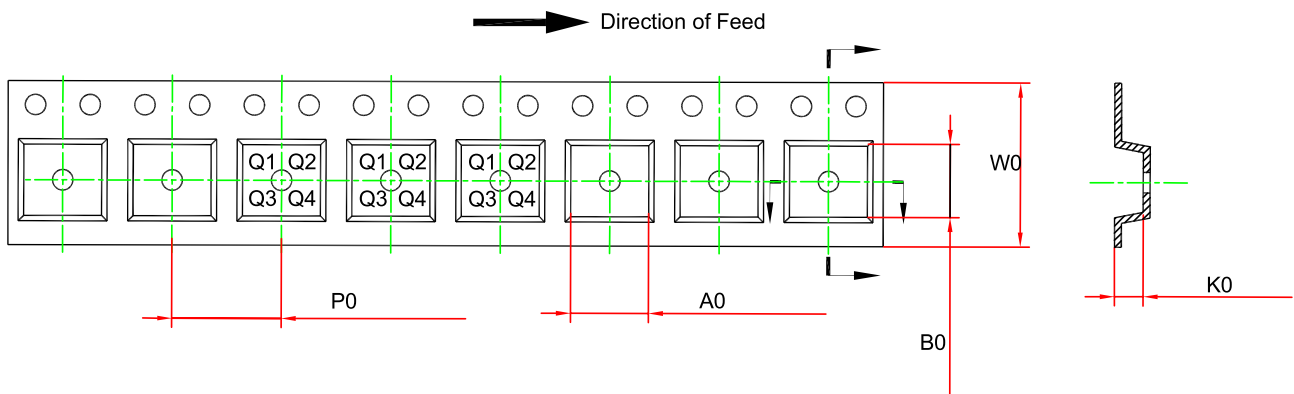
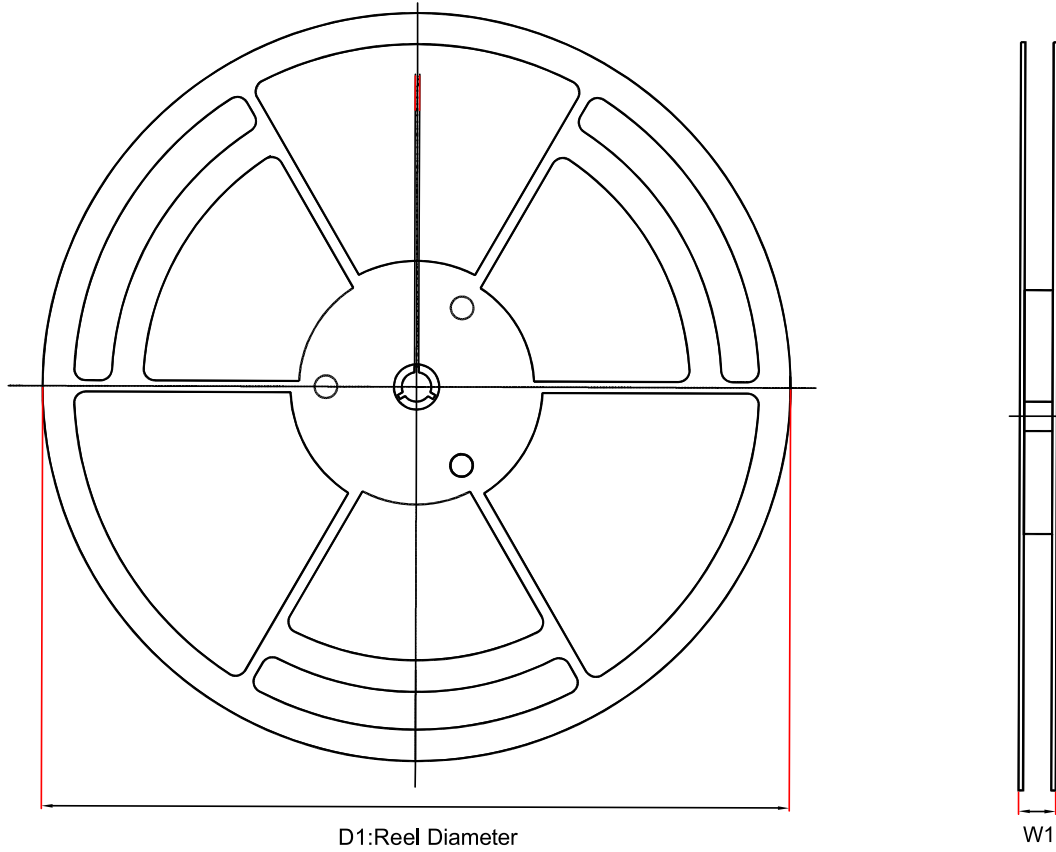


Figure 8. Typical Application Circuit

High-Voltage Sense and Low-IQ Supervisory Circuits with Programmable Reset Delay

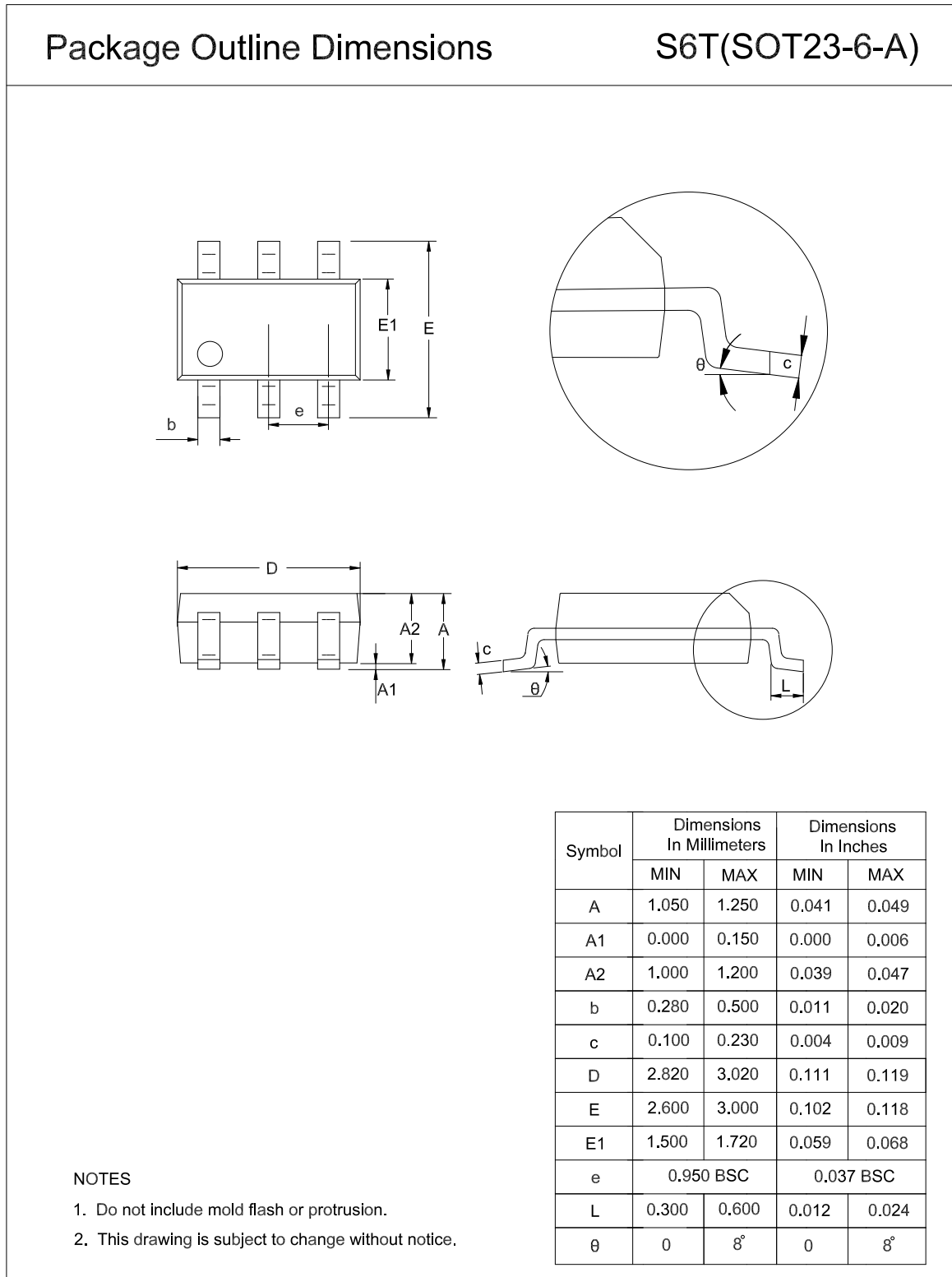
Tape and Reel Information



Order Number	Package	D1 (mm)	W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	W0 (mm)	Pin1 Quadrant
TPV8368XXX-S6TR-S	SOT23-6	178.0	12.3	3.2	3.2	1.4	4.0	8.0	Q3

High-Voltage Sense and Low-IQ Supervisory Circuits with Programmable Reset Delay

Package Outline Dimensions

SOT23-6


High-Voltage Sense and Low-IQ Supervisory Circuits with Programmable Reset Delay
Order Information

Order Number	Operating Temperature Range	Package	Marking Information	MSL	Transport Media, Quantity	Eco Plan
TPV8368SAQ-S6TR-S	-40 to 125°C	SOT23-6	Q00	3	Tape and Reel, 3000	Green
TPV8368SKQ-S6TR-S ⁽¹⁾	-40 to 125°C	SOT23-6	Q10	3	Tape and Reel, 3000	Green
TPV8368SPQ-S6TR-S ⁽¹⁾	-40 to 125°C	SOT23-6	Q15	3	Tape and Reel, 3000	Green
TPV8368SZQ-S6TR-S ⁽¹⁾	-40 to 125°C	SOT23-6	Q25	3	Tape and Reel, 3000	Green
TPV8368TBQ-S6TR-S ⁽¹⁾	-40 to 125°C	SOT23-6	Q27	3	Tape and Reel, 3000	Green
TPV8368TYQ-S6TR-S ⁽¹⁾	-40 to 125°C	SOT23-6	Q50	3	Tape and Reel, 3000	Green
TPV8368USQ-S6TR-S ⁽¹⁾	-40 to 125°C	SOT23-6	Q70	3	Tape and Reel, 3000	Green
TPV8368VMQ-S6TR-S ⁽¹⁾	-40 to 125°C	SOT23-6	Q90	3	Tape and Reel, 3000	Green
TPV8368VAQ-S6TR-S ⁽¹⁾	-40 to 125°C	SOT23-6	Q00	3	Tape and Reel, 3000	Green
TPV8368VXQ-S6TR-S ⁽¹⁾	-40 to 125°C	SOT23-6	Q10	3	Tape and Reel, 3000	Green
TPV8368WCQ-S6TR-S ⁽¹⁾	-40 to 125°C	SOT23-6	Q15	3	Tape and Reel, 3000	Green
TPV8368WMQ-S6TR-S ⁽¹⁾	-40 to 125°C	SOT23-6	Q25	3	Tape and Reel, 3000	Green
TPV8368WOQ-S6TR-S ⁽¹⁾	-40 to 125°C	SOT23-6	Q27	3	Tape and Reel, 3000	Green
TPV8368XLQ-S6TR-S ⁽¹⁾	-40 to 125°C	SOT23-6	Q50	3	Tape and Reel, 3000	Green
TPV8368YFQ-S6TR-S ⁽¹⁾	-40 to 125°C	SOT23-6	Q70	3	Tape and Reel, 3000	Green
TPV8368YZQ-S6TR-S ⁽¹⁾	-40 to 125°C	SOT23-6	Q90	3	Tape and Reel, 3000	Green

(1) For future products, contact the 3PEAK factory for more information and samples.

Green: 3PEAK defines "Green" to mean RoHS compatible and free of halogen substances.

High-Voltage Sense and Low-IQ Supervisory Circuits with Programmable Reset Delay

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